COMBINED EFFECT OF STRONTIUM CONTENT AND ELECTRODE TYPE ON ELECTRICAL PROPERTIES OF Ba_XSr_{1-X} TiO₃ THIN-FILMS

by

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ABSTRACT

Ferroelectric thin films have been on agenda of many research groups and the semiconductor industry for applications ranging from non-volatile data storage to tunable capacitors and antennas. In much of these works, the fact that ferroelectrics can be sensitive to the electrical boundary conditions, namely the type of the electrodes employed, is often taken for granted without an in-depth analysis. In this thesis work, we focus on $Ba_xSr_{1-x}TiO_3$ compositions, a well-known system, grown via a sol-gel method on conducting Nb:SrTiO₃ bottom electrodes to explore the effect of the Sr content and the top/bottom electrode asymmetry on the electrical characteristics of these systems. Following structural and morhphological characterization for quality check of the films, we extensively focus on a process called "resistive switching", which we repeatedly observed in our samples. Resistive switching phenomena is a topic of interest as the direction of the ferroelectric polarization determines whether there will be carrier depletion or accumulation in the semiconducting electrode (the conducting oxide bottom electrode in this thesis), upon which various novel device and transistor designs have been recently proposed by a number of groups. In this work, we focus on the experimental evidence for resistive switching in the light of thermodynamic theory of these systems and discuss the effect of Sr content on current-voltage characteristics of our samples.

STRANSİYUM İÇERİĞİ VE ELEKTROD TİPİNİN Ba_xSr_{1-x} TiO₃ İNCE FİLMLERİN ELEKTRİKSEL ÖZELLİKLERİ ÜZERİNE TÜMLEŞİK ETKİSİ

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Anahtar kelimeler: Ferroelektrik, baryum stransiyum titanat, sol-jel yöntemi, dirençli anahtarlama.

ÖZET

Ferroelektrik ince filmler, uçucu olmayan veri depolamadan ayarlanabilir kapasitörler ve antenlere kadar birçok araştırma grubunun ve yarı iletken endüstrisinin gündemindedir. Bu çalışmaların çoğunda, ferroelektriklerin elektrik sınır koşullarına, yani kullanılan elektrodların türüne hassas olabileceği, sıklıkla derinlemesine bir analiz yapılmadan ele alınır. Bu tez çalışmasında, Sr içeriğinin ve üst / alt elektrot asimetrisinin elektriksel karakteristikler üzerindeki etkisini araştırmak için Nb:SrTiO₃iletken alt elektrotları üzerinde bir sol-jel yöntemi ile geliştirilmiş iyi bilinen bir sistem olan Ba_xSr_{1-x}TiO₃ bileşimlerine odaklanıyoruz. Filmlerin kalite kontrolü için yapısal ve morfolojik karakterizasyonlarını takiben, "dirençli anahtarlama" adı verilen ve örneklerimizde tekrar tekrar gözlemlediğimiz bir sürece yoğunlaşıyoruz. Dirençli anahtarlama olgusu, ferroelektrik kutuplaşma yönünün yarıiletken elektrotta (bu tezdeki iletken oksit alt elektrotunda) taşıyıcıazalmasına veya birikmesine neden olup olmadığını belirlediği için ilgi çekici bir konudur ve bu konuda birçok grup tarafından çeşitli yeni cihaz ve transistör tasarımları önerilmiştir. Bu çalışmada, bu sistemlerin termodinamik teorileri ışığında dirençli geçiş için deneysel kanıta odaklanıyoruz ve örneklerin akım-voltaj karakteristikleri üzerindeki Sr içeriğinin etkisini tartışıyoruz.

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LIST OF SYMBOLS AND ABBREVIATIONS

- BCs Boundary Conditions
- BST Barium Strontium Titanate
- CAFM Conductive Atomic Microscopy
- C-V Capacitance-Voltage
- DRAM Dynamic RAMs
- FE Ferroelectric
- FeRAM Ferroelectric RAMs
- FTJ Ferroelectric Tunnel Junction
- HRS High Resistive State
- I-V Current-Voltage
- LGD Landau-Ginzburg-Devonshire
- LRS Low Resistive State
- LSMO lanthanum strontium manganite
- MDM Metal-Dielectric-Metal
- MFM Metal-Ferroelectric-Metal
- M-S Metal-Semiconductor
- P-E Polarization-Electric field
- PTO PbTiO₃
- PZT Lead Zirconate Titanate
- RS Resistive Switching
- SBH Schottky Barrier Height
- T_c Transition Temperature

Chapter 1: INTRODUCTION

Ferroelectrics are promising materials for a wide range of applications. These materials, unlike regular dielectrics, possess a remnant polarization in the absence of an external electric field in analogy with magnets where a remnant magnetization exists in the absence of an external magnetic field. Unlike magnetism, the ferroelectric polarization is due to asymmetric displacements of some of the ions in the unit cell when the material is cooled below a critical temperature. The polarization dependence of properties of ferroelectric materials have attracted much attention due to the possibility of application to various electronic devices such as memory cell capacitors [1]. As they present non-linear variation of dielectric constant with the electric field, high dielectric constant and moderate loss in microwave domain, ferroelectric materials present also a high potential for microwave applications [2], [3]. In microwaves, the ferroelectric materials exhibit lower dielectric loss in the paraelectric phase than in the ferroelectric phase. Therefore, most of the electrically controlled devices such as phase-shifters, tunable filters or ferroelectric varactors employ the ferroelectric materials in the paraelectric phase [4]. Barium Strontium Titanate (Ba_xSr_{1-x}TiO₃) (x=0, 0.5, 0.7) is one of the most researched ferroelectric materials for tunable applications at high frequencies since it demonstrates a superior tradeoff between loss and tenability [5]–[7].

In information technology, ferroelectrics are sometimes used as active components in high-density random access memories ferroelectric RAMs (FeRAM) and Dynamic RAMs (DRAMs) memories[8], [9]. In FeRAM a ferroelectric layer is used instead of a typical dielectric to achieve non-volatility via the presence of the polarization. FeRAM has many advantages over flash memories include: low power consumption, fast write process, longer read/write cycle ($10^{10}-10^{14}$ cycles) and etc. in 2002 Texas instrument introduced a new design of FeRAM to commercialize the product (Figure 1-1).However, the Like all prototypes, FeRAM has its own disadvantages include: it has lower storage densities comparing flash devices, limitation of storage capacity, production cost is higher cost. Also, FeRAM suffer an unusual technical disadvantage of a destructive read process, which requires a write-after-read architecture. Hence, the FeRAM improvement requires more investigations to modify the key characteristics of the ferroelectrics materials.



Figure 1-1: Cross section of a ferroelectric memory designed by Texas Instruments (TI).

Vast amount of the studies aimed to investigate the tunability of Barium Strontium Titanate structure via addition of Strontium. Even commercial powder in high purity is available in fair price which can be used as reliable precursor in a scientific investigation [10]–[14]. In the previous studies, the whole structural modification, dielectric change and electrical characteristics of such films regarding to the application needs are investigated thoroughly [2], [4]–[8]. The dielectric properties of BST films have often been interpreted without the electrode effects or the electronic nature of the interfaces (Schottky junction, Ohmic junction and etc.). Quite a number of studies including papers from our group have revealed that the presence of ferroelectric polarization has a strong impact on the nature of the electronic structure of the interfaces and that this becomes, in fact, a design parameter in such systems. Thus, the main concern in this thesis is to investigate the electrical properties such as the capacitance-voltage (C-V) and current-voltage (I-V) behavior of Ba_xSr_{1-x},TiO₃ films in relation to the electrode/ferroelectric film interface [15]–[17]. As the strength of the ferroelectric polarization determines the extent of the aim was to change the compositional structure combined with changing electrode type to magnify the plausible mechanism governs the electrical responses.

1.1 Definitions and Properties

Ferroelectricity is a property of materials that possess spontaneous electric polarization. It is due to presence of electric dipoles and applying external electric field changes the polarization. Ferroelectricity can be determined in two ways. First, ferroelectric structure has to be non-centrosymmetric. In a structure with centre of symmetry, any dipole moment generated in specific direction would be canceled by structure symmetry to zero. Second, there must also be a spontaneous local dipole moment (cause of macroscopic polarization), and central atom must be in a non-equilibrium position [18]. In a typical ferroelectric material, such as the pseudo-cubic perovskite oxide (BaTiO₃, for instance), ferroelectricity arises from the spontaneous displacement of the positively charged Barium (Ba) and titanium (Ti) cations against the negatively charged oxygen anions. Ferroelectric distortion originates from the bonding preferences of the Ti ion, which is slightly "too small" for its coordination cage inside the oxygen octahedra. Placing slightly off-center, the Ti ion can be in a better distribution of bond distances, hence the lowering of the Coulomb interactions, lowering the energy of the material. In

Figure 1-2, the Ti ion can equivalently move towards the "top" or the "bottom" of the cage, thereby leading to the "up" and "down" states, either of which has a lower energy than the undistorted configuration. (This picture is an oversimplification; in actuality, all of the atoms move and many forces contribute) Ferroelectric materials, such as BaTiO3, acquire an electric polarization because the crystal lowers its energy (blue curve) when positively charged ions (green and blue) displace relative to negatively charged ions (red). (Displacement of Ba ions not shown, for clarity) In most materials this energy gain is overwhelmed by the energy cost to the crystal (yellow curve) in the presence of the depolarization field—the internal electric field that comes from the displaced ions.



Figure 1-2: Energy state of lattice cell due to displacement of Ti atom in BaTiO3 structure [19].

Many ferroelectrics can't spontaneously polarize in vacuum, or often, in air. The reason is that if all of the positive cations move in the same direction, then positive charge will accumulate on one surface and negative charge on the other. Gauss's law tells us that these charges will produce a strong electric field that counteracts the displacement of the cations, essentially pushing them back into their positions in the non-ferroelectric structure. The depolarization field has a large and positive electrostatic cost, which can overwhelm the energy gained through the ferroelectric distortion. In ferroelectric materials less than about 10 nanometers in thickness, even a small residual depolarization field can completely suppress ferroelectricity [20], [21]. Non-electroded films can develop stripe domain structures where the polarization sign alternates along the sample plane at a given period [22]. Such a "domain" formation is favorable as the alternating sign of polarization charges at the surfaces can minimize the depolarizing fields and allow the stable ferroelectric state to exist. One other way to suppress the depolarization field is to sandwich the ferroelectric between short-circuited metallic electrodes, so that the free charges at the electrode surfaces exactly neutralize the polarization charges. Device applications of ferroelectrics almost always require the ferroelectric to be in contact with an electrode at least on one side, providing some degree of screening of polarization charges in proportion to the "ideality" of the electrode (whether electric field can penetrate into the electrode strongly or not). In real devices, however, this screening can never be perfect and a residual depolarization field remains. In fact, the energetic cost of the depolarization field is so high that most known materials (with some exceptions [23], [24]) lose their single-domain ferroelectric properties unless the surface charges are perfectly screened, a requirement that is very difficult to satisfy in practice. A multi-domain ferroelectric state can exist but is usually not preferred in device applications where the attractive properties of a single domain state are of interest. Ferroelectric domains can coexist along a specific crystallographic direction, in which certain atoms e.g. (Ti atoms in case of BaTiO₃) displaced along given axis, leading to a dipole moment in that direction. Depending on the crystal system, there may be few or many possible axes. In ferroelectric domains, dipole moments of the unit cells in one region lie one of the six directions with respect to another region, usually in an antiparallel configuration and a cross section through such crystal illustrated in Figure 1-3.



Figure 1-3: An illustration of ferroelectric domains in a crystal cross section.

1.2 Structure of Barium Titanate

Barium titanate (BaTiO₃) has different crystal structures (unit cells) at different temperatures. Going from a high temperature to lower temperatures phases are cubic, tetragonal, orthorhombic, and rhombohedral crystal structure. All expect the cubic structure is ferroelectric where the polarization direction corresponds to one of the crystallographic directions as depicted in Figure 1-4.



Figure 1-4: Change with temperature of the dielectric permittivity of a BaTiO₃ single crystal. The schematics of Ti displacement in the oxygen octahedron of the perovskite structure are also shown [25].

All of the phases have the ferroelectric effect except the cubic phase. The high temperature cubic phase is easier to demonstrate the location of the atoms. It consists of octahedral TiO_6 centers that define a cube with Ti vertices and Ti-O-Ti edges. In the cubic phase, Ba^{2+} is located at the center of the cube, with a coordination number of 12.

Above 120°C, cubic form of BaTiO₃ has regular octahedrons of O²⁻ ions around Ti⁴⁺ ion and has a center of symmetry. As a result, the six Ti-O dipole moments along $\pm x$, $\pm y$, $\pm z$ cancel each other and the material in such a state is called paraelectric. Below 120°C, cubic phase of BaTiO₃ transforms to a tetragonal (noncentrosymmetric phase) which one of the axis becoming longer, usually referred as z-axis or [001]-direction. Unilateral displacement of the positively changed Ti⁴⁺ ions against surrounding O²⁻ ions occurs to give rise to net permanent dipole moment. Coupling of such displacements and the associated dipole moment is a necessity for ferroelectricity. This transformation forces Ti ions go to lower energy off center positions, giving rise to permanent dipoles (Figure 1-5).



Figure 1-5: Distortion in BaTiO₃ upon cooling from cubic phase.

Since the distorted octahedrals are coupled together in ferroelectric phase, there exists a noticeable spontaneous polarization, ~25 μ C/cm², leading to a large dielectric constant, ~160, and large temperature dependence of dielectric constant.

BaTiO₃ shows two other structural transitions while cooled down below 120°C. First transition is orthorhombic structure at \sim 5°C and then to a rhombohedral structure at \sim -90°C. As a result of alteration in the symmetry, the polarization vector also changes from [001] in tetragonal to [110] in orthorhombic and [111] in rhombohedral structure.

1.3 Structure of Barium Strontium Titanate

The most important and common feature of the ferroelectric perovskite oxides is that they have metal-oxygen bond in form of octahedron in the unit cell which reported to be source of ferroelectric characteristic in such materials. Passing Curie point to the lower temperatures causes a small displacement of cations with respect to the position of the anions [26]. As a result of such displacement, a net dipole moment between the mass centers of the ions occurs. Here, the long range Coulomb forces are reported to be the source of ferroelectricity [27]. In a simplified definition, ferroelectricity in perovskite oxides is correlated to the tetragonality of the lattice. Tetragonality can be expressed as the ratio of the lattice parameter along the direction of applied electric filed over the lattice parameter along the direction perpendicular to the applied electric field. Doping and changing the composition are one of the effective ways to change the tetragonality of bulk perovskite oxide materials and hence alter the ferroelectricity [28], [29]. On the other hand, in the thin film deposition, effect of the substrate and size effect have also very significant importance [30]. Thus, composition and the substrate effects (through the misfit) exhibit an interplay that do impact the electrical properties of these systems, the Curie point in particular.

Barium Strontium Titanate (BST) in different composition ($Ba_xSr_{1-x}TiO_3$) is a solid solution of Barium Titanate ($BaTiO_3$) and Strontium Titanate ($SrTiO_3$) which can be formed in entire range of x (x=0,0.3,0.5). Although both $BaTiO_3$ and $SrTiO_3$ possess the Perovskite structure, the BST structure is considered as a complex perovskite structure due to the presence of the Sr^{2+} and Ba^{+2} in A-site of the lattice structure. In perovskite structures, the lattice constant is always assumed near 4Å due to the oxygen ionic radius of the 1.35Å.

The structure and property relationship in bulk state of BST has been well reviewed in the studies [31]–[33]. The ferroelectric transition temperature is nearly a linear function of Ba/Sr ratio (Figure 1-6). The shaded region in Figure 1-6 is considered as the region of interest in this thesis. Adding Sr into BTO structure causes the paraelectric to ferroelectric transition temperature T_c decrease linearly and 30% of STO, the transition happens near the room temperatures.



Figure 1-6: Ferroelectric transition temperature as a function of Ba/Sr ratio in bulk BST [34].

Figure 1-7 represents the crystal structure of BST composition as a function of Ba/Sr ratio. At the Ba-rich side, the structure has a tetragonal unit cell and at the Sr-rich side, the structure has a cubic unit cell. The separation edge between tetragonal and cubic symmetry is around x=0.3 which consist with the reported results.



Figure 1-7: Crystal structure of BST composition as a function of Ba/Sr ratio [34].

The dielectric constant of BST composition is significantly large, especially near the Curie temperature. For BST with x = 0.60, for example, the maximum dielectric constant is ~15,000 at room temperature (RT) as this is the composition that happens to have a Curie point close to RT. High dielectric constant of BST composition makes it a promising material for capacitors applications. Figure 1-8 represents the detailed information about temperature dependence of dielectric constant of BST composition.



Figure 1-8: Temperature dependence of dielectric constant for BST ceramics with different Ba/Sr ratio [34].

1.4 Electrical Properties

1.4.1 Polarization-Electric Field (P-E) Loops

Domains' ability to switch from one state to another under external electric field can be observed by a measurement of the polarization as a function of an magnitude of electric field, in which it takes the form of a hysteresis loop [35]. Hysteresis (P-E) loops are obtained from measurements of charge flowing through a sample as a result of a time-dependent voltage applied across it. Charge–voltage schematics for some common circuit are shown in Figure 1-9. For an ideal linear capacitor the charge is proportional to the electric field voltage so the loop is a straight line whose gradient is equal the capacitance of the material and is proportional to the permittivity (Figure 1-9-a).

According to Ohm's Law, the current is proportional to the voltage for an ideal resistor. Therefore, depending on time, there is an amount of charge that flows and a phase difference between the charge and the electric field for a cyclical voltage. Because of this the loop is a circle with the centre at the origin. In this case the measured charge relates to the current rather than the polarization (Figure 1-9-b). The loop in Figure 1-9-c belongs to a lossy capacitor, where the area within the loop is proportional to the loss tangent of the device, and the slope proportional to the capacitance. Loss can be due to dielectric hysteresis or leakage current or both. Figure 1-9-d shows the loop for a true ferroelectric. These loops are usually centered on zero and for both the lossy capacitor and the ferroelectric they cross the y axis at a non-zero value. In the ferroelectric case this crossing point provided a measure of the remanent polarization. For the lossy capacitor the non-zero crossing point does not indicate any remanence. Caution must be exercised in interpreting the crossing point on the charge (polarization) axis as ferroelectric remanence, particularly where there may be leakage currents or the ferroelectric behavior is not clearly established. There are many examples where lossy dielectric loops have been incorrectly presented as evidence of ferroelectric behavior [17], [36], [37].

Information, about tuning driving components like piezoelectric, can be provided by the P-E loop, on the capacitance and loss of a device at high fields and at different frequencies. In applications, such as thin film ferroelectric memories, the crucial parameters can be defined and the long and short term performance of the devices can be investigated by the help of the hysteresis of the material and measurement of the P-E loop.



Figure 1-9: Schematic charge voltage response (in arbitrary units) of a) linear capacitor, b) resistor, c) lossy capacitor and d) ferroelectric[38].

Since polarization is a function of external applied electric field, hysteresis loop diagram interpretation is based on Polarization (P) and external electric field (E). In Figure 1-10, in the origin P = 0 and E = 0. Ramping up the field (Path 1), the polarization gradually increases towards a saturation point (Path 2) in which electric field coherently orients all unit cells. By further increase in the field, dielectric charging enhances the polarization (Path 3). In the point that field is decreased to zero, the polarization decreases, however a little of polarization remains fixed. In this point E=0 and polarization value is called the remnant polarization (+P_r). By increasing the field in the opposite direction, in a specific field intensity called the coercive field E_c , an abrupt switch occurs in polarization. By further increasing the field, the polarization saturates and a dielectric response is reached again to an extreme point (Path 6). In a repeated reverse cycling result of P-E characterization is symmetric, and $\pm E_c$ are mark points.





The most classical way to study the characteristic of the metal-film interface is place a dielectric between two metallic electrodes in a form of metal-dielectric-metal (MDM) (see Figure 1-11).



Figure 1-11: An illustration of metal-dielectric interface in an MDM configuration.

Here the discussion will be based on the ferroelectric materials which can be considered a wide band gap semiconductor [39]. Hence, at first a simple metal-semiconductor interface with no polarization induced effect will be discussed. Then a well-known model for study the effect of polarization will be presented. In section 1.4.4, a model based on thermodynamic approaches coupled with electrostatics and semiconductor equations for a given FE-electrode couple will be presented based on the previous studies [40], [41]. Although the importance of electrical and polarization boundary conditions (BCs) on properties of FE films is very well anticipated, only a handful of relatively recent studies have seriously tried to address their impact on the properties [41]–[44].

1.4.2 A Simple Metal-Semiconductor Interface

When a metal and a semiconductor are joined, they form a junction which may have two possible junction types as the result. Depending on the characteristics of metal and semiconductor used, they may form rectifying contact (Schottky barrier contact), which allows current to pass in one direction, or it could be Ohmic contact, in which case current can pass in either direction. Here, we focus on Schottky junction and consider the contact of a metal and semiconductor (n-type or p-type) and interface characteristics of it. When a metal and semiconductor are brought together, the Fermi energies of the metal and the semiconductor reaches a common equilibrium value. This is only possible via electron transfer from one to the other. In case of a metal/n-type junction as an example, electrons in the *n*-type semiconductor can lower their energy by traversing the junction. As the electrons leave the semiconductor, due to the ionized donor atoms a positive charge will be left behind. A negative field is created by this charge and the band edges of the semiconductor into the metal and the drift of electrons caused by the field created by the ionized impurity atoms. A constant Fermi energy throughout the structure characterizes this equilibrium. In a similar manner, contacting the metal with different work function compare to semiconductor type forms various profiles of band bending in the interface (Figure 1-12).



Figure 1-12: Band bending before and after metal-semiconductor contact. a) High work-function metal and n-type semiconductor, b) low work-function metal and p-type semiconductor, and d) low work-function metal and p-type semiconductor [45].

Since the electrostatic analysis of a metal-semiconductor junction provides knowledge about the charge and field in the depletion region, it is worth looking into. The capacitance-voltage characteristics of the diode should also be obtained. The simple analytic model of the metal-semiconductor junction is based on the full depletion approximation which is obtained by assuming that the semiconductor is fully depleted over a given distance, called the depletion region. While an accurate charge distribution is not provided by this assumption, very reasonable approximate expressions for the electric field and potential throughout the semiconductor are provided.

The full depletion approximation is now applied to an M-S junction containing an *n*-type semiconductor. The depletion region is defined to be between the metal-semiconductor interface (x = 0) and the edge of the depletion region ($x = x_d$). The depletion layer width, x_d , is unknown at this point but will later be expressed as a function of the applied voltage.

The charge density in the semiconductor is the starting point to find the depletion layer width, followed by the calculation of the electric field and the potential across the semiconductor as a function of the depletion layer width. Afterwards, the depletion layer width will be solved by requiring the potential across the semiconductor to equal the difference between the built-in potential and the applied voltage, $\phi_i - V_a$.

As the semiconductor is depleted of mobile carriers within the depletion region, the charge density in that region is due to the ionized donors. Outside the depletion region, the semiconductor is assumed neutral. This yields the following expressions for the charge density ρ :

$$\rho(x) = qN_d \quad 0 < x < x_d
\rho(x) = 0 \qquad x_d < x$$
(1-1)

where full ionization is assumed so that the ionized donor density equals the donor density, N_d . This charge density is shown in Figure 1-13(a). The charge in the semiconductor is exactly balanced by the charge in the metal, Q_M , so that no electric field exists except around the metal-semiconductor interface.





Using Gauss's law, the electric field is obtained as a function of position, also shown in Figure 1-13(b).

$$\varepsilon(x) = -\frac{qN_d}{\varepsilon_s}(x_d - x) \quad 0 < x < x_d$$
(1-2)

$$\varepsilon(x) = 0 \quad x_d \le x \tag{1-3}$$

where ε_s is the dielectric constant of the semiconductor. It is also assumed that the electric field is zero outside the depletion region, since a non-zero field would cause the mobile carriers to redistribute until there is no field. The depletion region does not contain mobile carriers so that there can be an electric field. The largest (absolute) value of the electric field is obtained at the interface and is given by

$$\varepsilon(x=0) = -\frac{qN_d x_d}{\varepsilon_s} = -\frac{Q_d}{\varepsilon_s}$$
(1-4)

where the electric field is also related to the total charge (per unit area), Q_d , in the depletion layer. Since the electric field is minus the gradient of the potential, the potential is obtained by integrating the expression for the electric field, yielding:

$$\phi(x) = 0 \qquad x \le 0$$

$$\phi(x) = \frac{qN_d}{2\varepsilon_s} [x_d^2 - (x_c - x)^2] \quad 0 < x < x_d$$

$$\phi(x) = \frac{qN_d x_d^2}{2\varepsilon_s} \qquad x_d \le x$$
(1-5)

It is now assumed that the potential across the metal can be neglected. The thickness of the charge layer in the metal is very thin because the density of free carriers is very high in a metal. Although the total amount of charge is the same in both regions, the potential across the metal is several orders of magnitude smaller than that across the semiconductor. The built-in potential, ϕ_i in thermal equilibrium is equaled by the total potential difference across the semiconductor and is further reduced/increased by the applied voltage when a positive/negative voltage is applied to the metal. This boundary condition provides the following relation between the semiconductor potential at the surface, the applied voltage and the depletion layer width:

$$\phi_i - V_a = -\phi(x=0) = \frac{qN_d x_d^2}{2\varepsilon_c}$$
(1-6)

Solving this expression for the depletion layer width, *x*_d, yields:

$$\mathbf{x}_{d} = \sqrt{\frac{2\varepsilon_{s}(\phi_{i} - V_{a})}{qN_{d}}}$$
(1-7)

This model effectively explains a simple metal-semiconductor junction charge depletion and accumulation state without considering the effect of the ferroelectricity. However, the main concern in this thesis is to focus on a metal-ferroelectric-metal interface which will be discussed in the following section.

1.4.3 A model for Metal-Ferroelectric-Metal Interface

The Metal-Ferroelectric-Metal (MFM) heterostructure is a basic assembly to investigate the electrical characterization of target ferroelectric. In this prototype, metal electrodes deposited in such way that ferroelectric polarization is perpendicular on metal electrodes. Considering the ferroelectricity in such materials, electric dipoles should be oriented in head-to-tail shape inside the ferroelectric. Therefore, near one electrode there will be negative charge, and positive charge on the other interface. As a result of that, polarization state affects the classical quantities regards to metal-semiconductor Schottky contacts. Pintilie *et al.* [46] discuss a model based on metal-semiconductor interface which considers charges associated to ferroelectric polarization are present near the electrode interfaces. These charges will affect the quantities specific to classic metal-semiconductor Schottky contacts. This model was developed to take into consideration the effect of polarization charges on the interface properties [47].

According to the mentioned model, electrostatic properties of Schottky contacts are explained in the following equations:

$$V_{bi} = V_{bi} \pm \frac{P}{\mathcal{E}_0 \mathcal{E}_{st}} \delta$$
(1-8)

Here $V_{bi}^{'}$ is built in potential; V_{bi} is built in potential in absence of polarization; P is ferroelectric polarization and δ is distance of charge sheet in metal-ferroelectric physical interface. ε_0 is permittivity of the vacuum and ε_{st} is static dielectric constant.

$$E_{m} = \sqrt{\frac{2qN_{eff}(V + V_{bi})}{\varepsilon_{0}\varepsilon_{st}}} \pm \frac{P}{\varepsilon_{0}\varepsilon_{st}}$$
(1-9)

Here E_m is maximum field at the interface where q is the electron charge; N_{eff} is the effective density of space charge in the depleted region which not only consider the capacitors and ionized donors, but also consider the trapping centers carrying a net charge after capturing a charge barrier.

$$\omega = \sqrt{\frac{2\varepsilon_0 \varepsilon_{st} (V + V_{bi})}{q N_{eff}}}$$
(1-10)

Here ω is the width of the depletion region. Presence of polarization charges term affects the specific quantities earlier. The effect of polarization is not symmetric at the both interface due to the opposite signs of polarization charges. It causes a symmetrical system with the same metal electrode type on both sides which shows asymmetrical characteristics. The interface with positive charge imposes a considerable band bending since positive charges reject the hole from the interface. This rejections cause larger built-in potential comparing to polarization absence. At the other interface, smaller band bending is observable due to the attraction of holes by negative charges. Figure 1-14 is a band diagram schematic of such structure. In this diagram V_{bi} is the built-in voltage in the absence of the

ferroelectric polarization; V_{bi} is the built-in voltage with polarization and Φ_{bi}^{0} is the potential barrier in the absence of the ferroelectric polarization



Figure 1-14: Schematic of the band diagram for a metal-ferroelectric-metal structure [46].

The model presented in this section very well explains a metal-ferroelectric-metal junction behavior before and after applying an external bias. However, this model based on the apriori assumptions was not satisfactory for our approach towards explaining the ferroelectric-metal interface characteristics under the external applied bias. Hence, in the following section we propose a computational study based on a thermodynamic model coupled with the universal equations of electrostatics and equations of semiconductors which allow us to obtain results without any prior assumption.

1.4.4 Theory and Methodology for Computational Study

Here, we develop the thermodynamic theory for a given $Ba_{1-x}Sr_xTiO_3$ composition using the phenomenological coefficients of $BaTiO_3$ and $SrTiO_3$. The schematic of the $Ba_{1-x}Sr_xTiO_3$ heterostructures analyzed here are provided in Figure 1-15.

$$S^{f} = (1 - f).S^{PT} + f.S^{ST}$$
(1-11)

Where s^{f} is any material parameter, such as the band gap, thermodynamic stiffness coefficients, unit cell lattice parameter, of a layer corresponding to a particular fraction, *f*, of Sr replacing Ba ions in the lattice, s^{PT} and s^{ST} are any given material parameter for pure BT and ST, respectively. *f* varies in this model from 0 to 0.7, corresponding to a range of Curie temperatures from that of pure BT in bulk (~765K) all the way to ~ 200K for *f*=0.7. Averaging of material properties, both thermodynamic, structural and electronic, is an approximation to serve as a means to provide us with the results that can be used to interpret certain electrical behavior observed in these structures. In such systems, we have computationally designed, the overall PE-FE phase transformation temperature (T_c) will depend on the elastic and electrostatic BCs. As the experiment shows that all compositions considered in this thesis, namely BaTiO₃, Ba_{0.7}Sr_{0.3}TiO₃ and Ba_{0.5}Sr_{0.5}TiO₃ are all ferroelectric and we shall choose a misfit strain with the substrate accordingly to be compatible with the experimental observations (Please see the C-V measurement data in section3.2.2, Figure 3-20). In Figure 1-15 the total thickness of the films are approximately 100 nm. The material parameters of a given composition of Ba_{1-x}Sr_xTiO₃ are assumed to be weighted linear average of the constituents, namely BT and ST in the following manner:



Figure 1-15:An illustration of considered profile for computational study.

We assign a small compressive misfit of -1% to each structure allowing us to treat all compositions in their respective ferroelectric regimes. We start our thermodynamic treatment by writing the total energy density of a FE heterostructure on a substrate, which is essentially the sum of the free energies of various origins (polarization, electrostatic energy, electromechanical energy and etc.):

$$F_{\rm S} = \int_{V} F_{\rm FE}^{f} dV \tag{1-12}$$

Where the integration is over the volumetric free energy of the FE film layer and F_{FE}^{f} is the Landau-Ginzburg-Devonshire (LGD) energy of a given composition expressed as:

$$F_{FE}^{f} = \alpha_{1} \left(P_{1}^{2} + P_{2}^{2} + P_{3}^{2} \right) + \alpha_{11} \left(P_{1}^{4} + P_{2}^{4} + P_{3}^{4} \right) + \alpha_{12} \left(P_{1}^{2} P_{2}^{2} + P_{1}^{2} P_{3}^{2} + P_{2}^{2} P_{3}^{2} \right) \\ + \alpha_{111} \left(P_{1}^{6} + P_{2}^{6} + P_{3}^{6} \right) + \alpha_{112} \left[P_{1}^{4} \left(P_{2}^{2} + P_{3}^{2} \right) + P_{2}^{4} \left(P_{1}^{2} + P_{3}^{2} \right) + P_{3}^{4} \left(P_{1}^{2} + P_{2}^{2} \right) \right] \\ + \alpha_{123} P_{x}^{2} P_{y}^{2} P_{z}^{2} - \frac{1}{2} S_{11} \left(\sigma_{1}^{2} + \sigma_{2}^{2} + \sigma_{3}^{2} \right) - S_{12} \left(\sigma_{1} \sigma_{2} + \sigma_{1} \sigma_{3} + \sigma_{2} \sigma_{3} \right) - \frac{1}{2} S_{44} \left(\sigma_{4}^{2} + \sigma_{5}^{2} + \sigma_{6}^{2} \right) \\ - Q_{11} \left(\sigma_{1} P_{1}^{2} + \sigma_{2} P_{2}^{2} + \sigma_{3} P_{3}^{2} \right) - Q_{12} \left[\sigma_{1} \left(P_{2}^{2} + P_{3}^{2} \right) + \sigma_{2} \left(P_{1}^{2} + P_{3}^{2} \right) + \sigma_{3} \left(P_{1}^{2} + P_{2}^{2} \right) \right] \\ + F_{G}^{f}$$

$$(1-13)$$

where

$$F_{G}^{f} = G_{33} \left(\frac{dP_{3}}{dz}\right)^{2} + G_{31} \left(\frac{dP_{3}}{dx}\right)^{2} + G_{13} \left(\frac{dP_{1}}{dz}\right)^{2} + G_{11} \left(\frac{dP_{1}}{dx}\right)^{2} + G_{23} \left(\frac{dP_{2}}{dz}\right)^{2} + G_{21} \left(\frac{dP_{2}}{dx}\right)^{2} + G_{22} \left(\frac{dP_{2}}{dy}\right)^{2} + G_{12} \left(\frac{dP_{1}}{dy}\right)^{2}$$

$$(1-14)$$

In Eq. 1-3 and Eq.1-4, P_i (*i*=1,2,3) is the polarization vector, σ_i (*i*=1,2,...,6) is the (applied) stress tensor in contracted (Voigt) notation, S_{ij} and Q_{ij} are the elastic compliances (again in Voigt notation) at constant polarization and electrostrictive coefficients, respectively, α_i , α_{ij} , and α_{ijk} are dielectric stiffness (Landau) coefficients, and G_{ij} are gradient energy coefficients. For any given composition corresponding to an f, these parameters are determined from Eq. 1-11.

Taking into account that the top surface of the films are traction free results in some simplification since σ_3 , σ_4 , σ_5 , and σ_6 vanish, leaving us only with the in plane misfit stresses σ_1 and σ_2 that can be expressed in terms of the in-plane (polarization-free) misfit strain u_m for each layer such that $\sigma_1=\sigma_2=C_{11}u_m+C_{12}(u_m+u_3)$ where u_3 is the stress-free out-of-plane strain and C_{ij} are elastic constants at constant polarization. We point out that u_m can vary with composition in accordance with lattice parameters obtained via Eq. 1-11 which we denote as u_m^f but for convenience, we fix this for all layers for convenience as it suffices to reveal the trends in experiments as we shall show later on. Moreover, we assume that the gradient energy coefficients are isotropic and approximately the same for all compositions not to complicate further the analysis. In addition, variations in gradient energy due to possible anisotropy of this coefficient are negligibly small compared to electrostatic energy for the range of parameters (film thickness and etc.) considered in this thesis and by no means change the physics of the problem. Eq.1-13 can be written in terms of elastic compliances and in any given layer, $dF_{FE}^{f} / d\sigma_{i} = u_{m}^{f}$ (*i* = 1, 2) to express the in-plane stresses σ_{1} and σ_{2} in terms of u_{m}^{f} and P_{i} reducing Eq. 1-13 to:

$$F_{FE}^{f} = \alpha_{1}^{fm}(P_{1}^{2} + P_{2}^{2}) + \alpha_{3}^{fm}P_{3}^{2} + \alpha_{11}^{fm}(P_{1}^{4} + P_{2}^{4}) + \alpha_{33}^{fm}P_{3}^{4} + \alpha_{13}^{fm}(P_{1}^{2}P_{3}^{2} + P_{2}^{2}P_{3}^{2}) + \alpha_{12}^{fm}P_{1}^{2}P_{2}^{2} + \alpha_{111}(P_{1}^{6} + P_{2}^{6} + P_{3}^{6}) + \alpha_{112}[P_{1}^{4}(P_{2}^{2} + P_{3}^{2}) + P_{2}^{4}(P_{1}^{2} + P_{3}^{2}) + P_{3}^{4}(P_{1}^{2} + P_{2}^{2})] + \alpha_{123}P_{1}^{2}P_{2}^{2}P_{3}^{2} + u_{m}^{2}/(S_{11} + S_{12}) + G\left[\left(\frac{dP_{3}}{dz}\right)^{2} + \left(\frac{dP_{3}}{dx}\right)^{2} + \left(\frac{dP_{1}}{dz}\right)^{2} + \left(\frac{dP_{1}}{dx}\right)^{2}\right]$$
(1-15)

where α_{ijk}^{fm} are the misfit modified phenomenological coefficient of the landau Ginsburg energy of ferroelectric film [25]. In addition, the potential in Eq.1-15 has to be minimized according to

$$\frac{dF_{FE}}{dP_i} - \sum_i \frac{d}{dx_i} \left(\frac{dF_{FE}}{dg_i} \right) = 0$$
(1-16)

With $g_i = dP_i / dx_i$ (*i*=1,2,3). The problem can be reduced to two dimensions keeping in mind that these systems typically have symmetry with respect to one of the dimensions[48], [49] and are grown on [001] Nb:SrTiO₃ substrates, meaning a cross-section through the stack exposing one of the (100) or (010) planes with (001) being the base plane along the interface will be sufficient to study the properties of the compositions of interest in this thesis. Doing so and applying the procedure in Eq.1-16 for a given layer yields Euler-Lagrange equations of state for a given Sr content as:

$$2\alpha_{3}^{fm}P_{3} + 4\alpha_{13}^{fm}P_{3}P_{1}^{2} + 4\alpha_{33}^{fm}P_{3}^{3} + 6\alpha_{111}P_{3}^{5} + \alpha_{112}(4P_{3}P_{1}^{4} + 8P_{3}^{3}P_{1}^{2}) + 2\alpha_{123}P_{3}P_{1}^{4} - G\left(\frac{\partial^{2}P_{3}}{\partial z^{2}} + \frac{\partial^{2}P_{3}}{\partial x^{2}}\right) = E_{3}^{f}$$

$$2\alpha_{1}^{fm}P_{1} + 2(2\alpha_{11}^{fm} + \alpha_{12}^{fm})P_{1}^{3} + 2\alpha_{13}^{fm}P_{1}P_{3}^{2} + 6\alpha_{111}P_{1}^{5} + 2\alpha_{112}[3P_{1}^{5} + 3P_{1}^{3}P_{3}^{2} + P_{1}P_{3}^{4}] + 2\alpha_{123}P_{1}^{3}P_{3}^{2} - G\left(\frac{\partial^{2}P_{1}}{\partial z^{2}} + \frac{\partial^{2}P_{1}}{\partial x^{2}}\right) = E_{1}^{f}$$

$$(1-18)$$

that link P_i to the components of the internal electric field vector E_i (*i*=1,2,3).

Using the Poisson relation $\nabla_j \cdot D_i = \rho$ (*i*,*j*=1,2,3) which correlates the dielectric displacement vector D_i (*i*=1,2,3) to the local free charge density ρ_i one can then establish links between the local (scalar) electrostatic potential ϕ and the polarization gradients such that:
$$\frac{d^2\varphi^f}{dx^2} + \frac{d^2\varphi^f}{dz^2} = \frac{1}{\varepsilon_b\varepsilon_0} \left(\frac{dP_1}{dx} + \frac{dP_3}{dz} - \rho(r) \right)$$
(1-19)

$$\rho(r) = q \left[-n^{-}(r) + p^{+}(r) + N_{d}^{+}(r) \right]$$
(1-20)

In the above relation, q is the elementary charge, $n^{-}(r)$ is the free electron density, $p^{+}(r)$ is the hole density, $N_{d}^{+}(r)$ is the ionized donor density, ε_{0} is dielectric permittivity of vacuum (in SI units), ε_{b} is a background dielectric constant (taken as 7 here) [50]. Each of these charge terms depend on the electronic band parameters and local electrostatic potential and are described using Fermi-Dirac distribution functions as:

$$N_D^+ = N_D \left[1 - \left(\exp\left(\frac{q(E_D - E_F - \varphi)}{kT}\right) + 1 \right)^{-1} \right]$$
(1-21)

$$n^{-} = N_{c} \left(\exp\left(\frac{q(E_{c} - E_{F} - \varphi)}{kT}\right) + 1 \right)^{-1}$$
(1-22)

$$p^{+} = N_{v} \left[1 - \left(\exp\left(\frac{q(E_{v} - E_{F} - \varphi)}{kT}\right) + 1 \right)^{-1} \right]$$
(1-23)

where N_c is the effective density of states at the bottom of the conduction band, N_V is the effective density of states at the top of the valence band, E_c is the energy of an electron at the bottom of the conduction band, E_V is the energy of an electron at the top of the valence band, E_F is the Fermi level, and E_D is the ionization energy of the donor site that is taken with respect to the bottom of the conduction band. Note that band edges here shift only due to the internal electrostatic potential and are considered to be free from variations in the dispersion relation due to the presence of impurities (including vacancies), which, according to Tingting *et al.*, can shrink the band gap energy by almost 0.5 eV[51], [52]. The thermodynamic coefficients and other material parameters used in the computations are provided in Table 1.[39].

Parameters	SrTiO₃	BaTiO₃	
Lattice parameter (nm)	0.3904	0.4004	
T _C (°C)	-250	130	
C (10 ⁵ °C)	8×10 ⁵	1.5×10 ⁵	
α ₁₁ (N m ⁶ /C ⁴)	6.8×10 ⁹	3.6×(T-175) ×10 ⁸	
α ₁₂ (N m ⁶ /C ⁴)	2.74×10 ⁹	-0.0345×10 ⁸	
α ₁₁₁ (N m ¹⁰ /C ⁶)	0	6.6×10 ⁹	
α ₁₁₂ (N m ¹⁰ /C ⁶)	0	18.14×10 ⁸	
α ₁₂₃ (N m ¹⁰ /C ⁶)	0	-7.45×10 ⁹	
S ₁₁ (10 ⁻¹² N/m ²)	5.546	8.3	
S ₁₂ (10 ⁻¹² N/m ²)	-1.562	-2.7	
S ₄₄ (10 ⁻¹² N/m ²)	9.24	9.24	
Q ₁₁ (m ⁴ /C ²)	0.0457	0.11	
Q ₁₂ (m ⁴ /C ²)	-0.0135	-0.043	
Q ₄₄ (m ⁴ /C ²)	0.00975	5.165×10 ⁻²	
g (10 ⁻¹⁰ J m ³ /C ²)	6	6	
Nv, Nc	10 ²⁵ , 10 ²⁵	10 ²⁵ , 10 ²⁵	
E _v , E _c , E _D (eV)	-7.1, -3.9,-4.0	-6.72,-4.0,-4.1	

Table 1: Material parameters and thermodyn	namic coefficients for	r BTO and STO used in the	calculations [53], [54]
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One needs to simultaneously solve Eq.(1-15)-(1-23) with the top-bottom interface polarization boundary conditions (BCs) given as:

$$\lambda \frac{\partial P_1}{\partial x} - P_1 = 0 \big|_{z=0,h}$$
And

$$\lambda \frac{\partial P_3}{\partial z} - P_3 = 0 \big|_{z=z=0,h}$$
(1-25)

where λ is the extrapolation length determining the extent of change of polarization along the film normal at the interface and *h* is the total thickness of the film. *h* is given as the product of the number of grid points along the *z*-axis (thickness) *n* and the distance between two grid points *d*, taken in our computations as 0.5 nm. The BCs for electrostatic potential are specified at the Ba_{1-x}Sr_xTiO₃ -electrode interfaces as 0V for short circuiting or $\phi = \pm V_{app}$ at top electrode (*z*=100 nm, *n*=200) while the bottom electrode (*z*=0 nm, *n*=1) is kept at zero (grounded) where V_{app} is applied voltage. Periodic BCs are employed along the plane of the structures for both the electrostatic relations and the polarization. Note that the amount of charge transfer between the FE and the electrode will depend on the Fermi level differences of the stack and the electrodes as well as direction of P_i and the internal built-in fields. As such, we resort to numerical methods to analyze the electrical and dielectric properties of the compositions of interest. Ideal metal electrodes are assumed to behave as top contacts for which work function is taken as that of Pt or Cu, common electrodematerials. The Fermi level of the stack is equilibrated with that of the Nb:SrTiO₃ as the substrate is very thick and is supposed to act as a reservoir of carriers due to its n-type nature. The charges due to the spontaneous polarization of the FE layer at the Ba_{1-x}Sr_xTiO₃ -top electrode interfaces are assumed to be partially screened in accordance with the formation of a thin dead layer that is the case with any metallic electrode. The average of amplitude of the out-of-plane polarization <| P_3 |> is saved after each run for a given *T* and position in the computational grid such that:

$$<|P_3|>=\frac{\sum_{N}|P_3(r)|}{N}$$
 (1-26)

where $N=200 \cdot n$ is the total number of sites. Such a resolution allows us unambiguously determine the transition temperature since the transition from a PE to a FE in case of a MD state will amount to a zero change of average polarization. The dielectric response of the gBST stacks at or far from T_c is computed via:

$$\varepsilon_{r}^{gFE} = \frac{(100 \times 10^{-9}) \left[D_{3}^{Top} @V_{App} + 0.001 - D_{3}^{Top} @V_{App} \right]}{\varepsilon_{0} \times 0.001}$$
(1-27)

at the end of 5000 iterations for a given *T*. Here 1 mV (0.001 V in Eq.1-27) in the denominator is the small signal bias and, therefore, 10⁴ V/m is the small signal electric field we use to probe the dielectric response of a 100 nm thick film, D_3^{Top} is the average dielectric displacement at the top gBST/electrode interface given by $\varepsilon_0 \varepsilon_b E_3^{Top} + P_3^{Top}$ with superscript "*Top*" indicating the very top row of the computational grid that is in contact with the top electrodeEq.1-27 contains V_{App} that, when non-zero, allows us to compute the dielectric response under bias. We employ a finite difference discretization and carry out a Gauss-Seidel iterative scheme to solve the coupled Eqs.1-11,(1-14,15) and (1-26,27) simultaneously subject to BCs discussed above. The computation grid consists of $N=200 \cdot n=40,000$ points for all films corresponding to a total film thickness of 100 nm. We terminate the solution after 5000 iterations that yield a difference of about 10^{-5} C/m² for P_1 and P_3 between two consecutive steps. The result of the computational study is compared to experimental results to shed the light on the obtained results in real life experiments. One should consider this is an ideal model which proposed only to clarify some facts about ferroelectric polarization state.

1.4.5 Capacitance-Voltage Characteristics

The C-V measurements have a butterfly-loop shape in the case of Metal-Ferroelectric-Metal heterostructure which can be interpreted as an indication of the ferroelectricity and polarization reversal. The voltage dependence of the capacitance is explained in two cases. First, in form of the net electric field dependence of the dielectric constant and second, by the voltage dependence of the depletion layers appear at the junction between the electrode and the ferroelectric interface. In the first case the ferroelectric layer is assumed as an ideal insulator. Whereas in the second case the ferroelectric layer is regarded as a semiconductor with a voltage dependent dielectric constant [55], [56]. Also, C–V measurement can be used to extract the reversible polarization, since the DC bias can set the polarization value, while a small AC voltage yields to domain walls reversible movements [57]. The origin of the capacitance peaks in the C-V characteristics attributed to various mechanisms, including the fact that peaks can occur also in Schottky contacts p-n junctions or even in non-ferroelectric structures [58]. Pintilie *et al.* [59] reported a possible link between the maxima in capacitance and the polarization reversal in detail. Their results show that the capacitance peaks and the butter fly shape directly lead to the irreversible polarization switching (

Figure 1-16). Also, capacitance peaks can be originated from trapping defects near the Schottky contact. However, this phenomena is reversible and butterfly shape should not appear.



Figure 1-16: C–V characteristics at 1 kHz measured after poling the film with 6 V for one minute [59].

1.4.6 Resistive Switching

Ferroelectric resistive switching (RS) or memristor effects have been observed in a number of studies and reported incessantly for several ferroelectric materials by several groups [60]–[62]. The technological applications of these materials such as resistive random access memories (RRAMs) have motivated researchers to focus on the subject. The reason is that while conventional RRAMs are functionally based on defect-mediated (ionic or electronic) process, ferroelectric RRAMs rely on the intrinsic ferroelectric domain switching instead of defects immigration[63]. Several mechanisms have been proposed to explain the resistive switching behaviors of ferroelectrics including formation/rupture of conductive filament-like structures (such as threading dislocation cores) [60], carrier-and/or defect-control of the depletion layer thickness[64] ferroelectric tunneling [65] polarization modulation of interface Schottky barrier[66]. Despite the proposed mechanism for RS in FE, the very origin of RS is still somewhat controversial, and the reported results in BTO films presenting some speculations about resistive switching [61], [67]. However, in the scientific community there is an agreement on presence of RS which can be classified into unipolar and bipolar switching according to number of degrees of freedom for an applied input. In this context, unipolar switching has only one degree of freedom: the amplitude of the applied input (either voltage or current), whereas bipolar switching involves two: the amplitude and the polarity of input. Unipolar and bipolar switching behaviors are presented by scalar and vector variable functions, respectively.

Sawa *et al.*[68] reported that the conductive filament mechanism in a Pt-Ni-Pt cell and compared the and carrier- and/or defect-control of the depletion layer thickness in a Ti-La₂CuO₄-La_{1.65}Sr_{0.35}CuO₄cell by considering the unipolar and bipolar switching respectively (Figure 1-17).



Figure 1-17: I-V curves for (a) unipolar (nonpolar) switching in a Pt-NiO-Pt cell and (b) bipolar switching in a Ti-La₂CuO₄-La_{1.65}Sr_{0.35}CuO₄cell. Proposed models for resistive switching which classified according to either (c) a filamentary conducting path, or (d) an interface-type conducting path [68].

Garcia *et al.* [69] considered a metal-ferroelectric-metal interface in order to investigate the RS by tunnel junction. In their model polarization charges are usually present at the surface of a ferroelectric and depending on their sign, they attract or repel electrons. The electrons near the interface screen the polarization charges in a short distance in the electrode. This distance is beyond the distance of electron density which resumes its normal value. According to Thomas–Fermi theory, the screening length is a function of the electronic density of states at the Fermi level. In metals, the Thomas–Fermi screening length can be around couple of angstroms. In semiconductors, this length can reach 1Å while screening is imperfect. However, Stengel *et al.* [70] emphasize that the actual effective screening length differs from the one defined by Thomas–Fermi. In this case, its microscopic properties of the ferroelectric-electrode interface which indicates the direction of the polarization (0> when *P* points to the interface). If we consider an ultrathin ferroelectric layer deposited between two different electrodes with more efficient screening on the left side than on the right side (Figure 1-18). The magnitude of the additional electrostatic potential at the electrode/ferroelectric interface is as same as the screening length over the dielectric constant of the electrode. In a simple analogy,

if we consider the initial electronic potential barrier rectangular (identical barrier heights for the left and right interfaces), we conclude that the polarization charge effects induce an asymmetric electronic potential profile [71], [72]. By reversing the polarization, the asymmetry of the electronic potential profile is reversed. Hence, the barrier height in average is higher when *P* points to the electrode (left) than when *P* points to the right side. Tunnel transmission depends exponentially on the square root of the barrier height, the junction resistive will depend on the direction of *P*. The asymmetry between the two different ferroelectric/electrode interfaces crucial to modulate the current transmission of the ferroelectric barrier. However, asymmetry may come from the interfaces because of different interface terminations[73]. Hence, the use of different electrode materials is not always mandatory.



Figure 1-18:The polarization-induced variation of the tunnel barrier height in FTJs and the potential profile across the metal 1 (M1)/ferroelectric(FE)/metal 2 (M2) heterostructure for two orientations (right and left) of the ferroelectric polarization (P) [74].

It is known that the *I*–*V* behavior of metal-Semiconductor-metal cells heavily relies on the electrode materials [62], [67], [75]. This implies the importance of property of the anode/Semiconductor (n-type) and the cathode/Semiconductor (p-type) interface for electronic carrier injection. The Schottky barrier formed at the metal/Semiconductor interface plays an important role in the electronic transport, mostly injection through the SBH. Ideally, the SBH is defined as the difference between the electron affinity of the Semiconductor and work function of the metal. The SBH is therefore supposed to be proportional to the work function of the metal. However, in reality there may be an interfacial layer between the metal and Semiconductor, which is equivalent to a thin-slab-shaped capacitor. A certain voltage drop through the layer occurs, leading to a deviation of the SBH from the ideal value as suggested by Cowley and Sze [76].

One of the first models attempting to explain ferroelectric-resistive switching reported for PTO according to the alteration of carrierdepletion width in ferroelectrics [77]. In their model, Meyer and Kohlstedt assumed a gap at the metal-ferroelectric interface, which separates the induced charge on the metal from the surface charge on the ferroelectric interface [78]. To be precise, they assumed a high depolarization field in their model. At one of the interfaces, interfacial gap is placed, i.e. asymmetric interface. Resulted asymmetry led to polarization-dependent profiles of the ferroelectric's conduction band which induced polarization-dependent conductivity of delocalized electrons in the ferroelectric. Another model for RS is FTJ which differs from aforementioned MFM junction and rely on current transport through the ferroelectrics is quantum mechanical tunneling [79].

The charges in the FE's conduction band with respect to polarization direction leads to a change in tunneling matrix element which this leads to RS [80]. Thus, the direction of polarization determines whether quantum mechanical tunneling will occur or not for a given bias scheme on the MFM stack. According to Kohlstedt *et al.* [80], one possible way to separate RS from ferroelectric switching is the simultaneous acquisition of electric currents and ferroelectric-switching-related responses, e.g. piezoelectric response, according to the applied voltage. They have simultaneously investigated the currents and piezoelectric responses of PZT with voltage (

Figure 1-19). According to this figure, the resistive and ferroelectric switching behaviors are not coupled. In other words, the simultaneous switching of the ferroelectric polarization and resistive at the same electric field would be a precondition (but not sufficient) for ferroelectric-resistive switching. In

Figure 1-19, the ferroelectric switching appears at about 170 kV cm 1 (0.5 V) and 170 kV cm 1 (0.5 V) applied electric fields, while the resistive switching can be observed at about 330 kV cm 1 (0.9 V) and 330 kV cm 1 (0.9 V) applied electric fields [80]. In

Figure 1-19 (b) the same measurement is applied on a 30 nm thick PZT film on a LSMO electrode. This measurement was conducted using a Conductive AFM (CAFM) tip. The curves show the correlation between the piezoelectric and current responses at a tip bias of approximately 3.3 V, suggesting ferroelectric-resistive switching.



Figure 1-19: Simultaneous measurements of current and piezoelectric response of a MFM cell with a 6 nm thick PZT film [81].

Mikheev *et al.* [82] reported a systematic study of RS of Pt and Nb-doped SrTiO₃. They demonstrate that RS is controlled by an interfacial layer, as shown by a parasitic interfacial capacitance, an increased ideality factor of the Schottky barrier and an extended depletion width within the SrTiO₃. In their study, a charge trapping-based model can fully explain the Schottky barrier lowering that accompanies the resistive switching. The interfacial layer capacitance is an important factor in controlling the magnitude of the effect. In this study, a series of Pt-Nb:SrTiO₃ devices (Figure 1-20) were investigated. The interface quality was changed intentionally across this series by changing the deposition parameters. The samples are labelled A–D in which sample A (highest quality interface) consists of an all-epitaxial junction of (001)Pt grown by high-temperature sputtering. All samples were annealed in O₂ to eliminate any possible contributions from oxygen vacancies.



Figure 1-20: (a) Schematic of the device. (b) *I*–*V* characteristics for all samples. The blue and orange lines fit to equation (1) for the, respectively, in each case. (c) Extracted barrier heights φ_B and ideality factors *n*. (d) Forward bias *I*–*V* for samples A and D. reprinted from [82].

Figure 1-20 (b) represents the I-V characteristics of the four Pt-Nb:SrTiO₃ junctions. The I–V hysteresis was probed by sweeping from 6V to +2V then to 6V. All samples reveal bipolar RS, with a high positive bias increasing the junction current and negative bias reversing the effect. The device RS states are referred to as low- and high-resistive states (LRS and HRS). The magnitude of the RS clearly decreases as the junction quality is improved.

To conclude, if the ferroelectric polarization points to the semiconductor, the n-type semiconductor surface will be driven by the positive bound charges in the ferroelectric-semiconductor interface into accumulation. The accumulated semiconductor can be treated as a metal and the screening is then similar to that in metal-ferroelectric-metal FTJ. The screening is generally not complete and in the ferroelectric barrier, a depolarization field opposite to the polarization develops which lowers the barrier height and generates a higher tunneling transmittance. The device is then set to a low resistive ON state (LRS) (Figure 1-21) [83].

As the polarization is reversed, pointing to the metal electrode, the semiconductor surface is depleted of electrons and the negative ferroelectric bound charges have to be screened by the immobile ionized donors. In contrast to the ON state, where majority carriers can be accumulated very close to the ferroelectric-semiconductor interface, the immobile screening charges in the depleted state spread over a space charge region defined by the doping profile. On one hand, the incomplete screening again produces a depolarization field, but this time increases the barrier height. On the other hand, the tunneling electrons have to experience an extra barrier in the depleted space charge region owing to band bending induced by ferroelectric polarization in the barrier. The tunneling transmittance can be greatly decreased by this extra barrier. The device is then set to a high resistive OFF state (HRS) (Figure 1-21)



HRS(OFF)

Figure 1-21: An illustration of low resistive state (LRS) and high resistive state (HRS) depending on the polarization direction.

Chapter 2: SAMPLE FABRICATION METHOD AND PROCEDURE

2.1 Solution Preparation Method

In order to deposit Barium Strontium Titanate (BST) films on various substrates, spin coating technique was used. In this study, three different compositions of BST films (BaTiO₃,Ba_{0.7}Sr_{0.3}TiO₃,and Ba_{0.5}Sr_{0.5}TiO₃) were produced by Sol-gel method. The sol-gel method has several advantages over other deposition methods in which better stoichiometry and homogeneity control can be achieved easier. Also, in combining with spin coating deposition technique, the cost and time of sample preparation decrease considerably [84].

In overall, there are two main methods to produce thin films of oxides using Sol-gel. First one is dispersing powder of fine particles and preparing a stable solution and then depositing the film. Second one is dissolving organometallic compounds in a proper solution in which polymerization of alkoxides produces a continuous network known as gel. In this thesis, the solution preparation was based on the second method using Barium, Strontium and Titanium alkoxides.

2.1.1 Barium Strontium Titanate (Ba_xSr_{1-x}TiO₃) Solution Preparation

For preparing the barium strontium titanate ($Ba_xSr_{1-x}TiO_3$) solution, using of the different alkoxides was proposed in literature. Table 2-1summarizes the list of used alkoxides reported.

Alkoxides	Solvent	Reference			
Barium acetate Strontium acetate Titanium acetate	Glacial acetic acid Acetyl acetone	Cheng <i>et al</i> . [85]			
Barium acetate Strontium acetate Titanium (IV) isopropoxide	Ethylene Glycol Glacial acetic acid	Tahan D. <i>et al</i> .[86]			
Barium acetate Strontium acetate Titanium (IV) isopropoxide	n-propanol Ethylene Glycol	E. Dien <i>et al</i> .[87]			
Barium carbonate Strontium carbonate Titanium (IV) isopropoxide	Ethylene glycol Citric acid	F. M. Pontes <i>et al</i> .[88]			
Barium acetate Strontium Acetate Tetrabutyl titanate	Methanol Glacial acetic acid Ethylene glycol dimethyl ether Acetyl acetone (stabilizer)	H. Tian <i>et al</i> . [89]			

Table 2-1: Summarized list of different alkoxides in reported studies.

All substances used in this thesis are used without any further purification. The information about starting substances is listed in Table 2-2.

Substance name	Substance information	
Barium acetate (Ba(AC) ₂)	Sigma-Aldrich, CAS #: 543-80-6	
Strontium acetate (Sr(AC) ₂)	ABCR, CAS #: 543-94-2	
Titanium (IV) isopropoxide (TIP)	Sigma-Aldrich, CAS #: 546-68-9	
Glacial Acetic Acid (GAA)	MERCK, CAS #: 64-19-7	
2-Propanol	MERCK CAS #: 67-63-0	

Table 2-2: Starting substances and information.

For preparing the solution, two separate solutions were prepared. The first solution was obtained by mixing the Ba(AC)₂, Sr(AC)₂with GAA. The solution was left for mixing on the stirrer for 1 hour in room temperature. The second solution was prepared by mixing the TIP and 2-propanol on the stirrer for 30 minutes in room temperature. After 1 hour, first solution was added to the second one and whole mixture was stirred for 1 hour in room temperature. The concentration of the obtained solution was 1M. In order to deposit higher quality films, 0.5M solution was prepared by diluting the 1M solution via adding GAA. The detailed information about the mixing ratio and sample coding are presented in Table 2-3.

Table 2-3: Chemical substance mixing ratio.

Sample Code	Composition	Ba(AC)₂(g)	Sr(AC)2 (g)	GAA (mL)	TIP (mL)	2-Propanol (mL)
ВТ	BaTiO₃	2.5542	0	10	3	15.5
BST70	Ba _{0.7} Sr _{0.3} TiO ₃	1.7879	0.6171	10	3	15.5
BST50	$Ba_{0.5}Sr_{0.5}TiO_3$	1.2771	1.0285	10	3	15.5

2.1.2 Thin-film Deposition Method

Here to deposit homogenous films, the spin coating technique was used. Spin coating process can be divided into four stages: deposition, spin-up, spin off, and evaporation respectively. The spin coating process is basically illustrated below.



Figure 2-1: Four stages of spin coating process.

In the deposition stage, solution covers the substrates' surface. Homogeneity of the prepared solution has critical effect on the quality of the deposited films and it's critical that solution wets the surface. In the spin-up stage, solution entirely covers the surface. Evaporation rate and viscosity of the solution affects the dispersion in this stage while substrate is accelerated up to the desired rotation speed, in which centrifugal force moves the solution radially outward. During the spin-off stage, arrangement of the atoms is in the manner close to final desired crystalline phase. Although the evaporation rate is the final stage, during the whole process, evaporation is inevitable. However, this stage determines the final thickness of the film. In this thesis, spin coating technique parameters were considered along with several criteria for solution:

- Solvent must have proper evaporating rate: the major part of the solvent should evaporate during the spinning stage and also an excess of liquid should dispense during this stage. Any remanent liquid on the surface cause inhomogeneous film surface.
- Solvent selection is critical since viscosity and evaporating rate directly affect the quality of the film. Hence, optimizing the solvent magnitude and matching the stoichiometry are the main challenges in producing homogenous solutions.
- Solvent should wet the surface. Controlling the pH and selection of the solvent have critical effects on the quality of the films.
- Spin acceleration must be optimized since the profile of the solution flow is highly dependent on the centrifugal force. Hence, here the three step spinning is used. First step is in slow rotation in which it lets the solution disperse on the surface. Second step is the main and the fast rotation step. Speed of the third step is decreased until the spin stops. The thickness of the initial film during the spin-off can be calculated in terms of [90] :

Here h_0 is the initial thickness, ω is angular velocity, t is time, solution viscosity μ and density ρ assumed as the constant values. According to the term, even if the film is not uniform in the beginning of the process, it tends to get uniform. Hence, slow spin at the beginning guaranties the homogenous dispersion of the solution on the surface, while high speed and longer spin time reduce the film thickness considerably. In this thesis, a Laurell spin coater model: WS-650-23B is used. The spin coating speed and time are optimized in forms of two steps. In the first step, spin speed is set to 500rpm for 15 seconds and next step 6000rpm for 60 seconds. In this thesis, six samples are prepared in three different compositions two of which are different metal electrodes deposited on them separately.

2.1.3 Thermal Treatment of Thin-films

Further heat treatment is required to crystallize the amorphous deposited films in the form of perovskite structure. It is important to optimize the heat treatment parameters such as: heating rate, pyrolysis temperature, final sintering temperature, soaking time in each temperature and medium atmosphere. Deposited films are dried in air at 200°C for 5 min on hot plate. Then they are heated up to 400°C by 5°C /min rate. All samples are kept in 400°C for 30 min. Then samples are heated up by the same rate to 800°C and kept for 150min. Whole heat treatment process includes heating up and cooling down in oxygen reach environment. The flowchart of whole sample preparation process is shown in Figure 2-2.



Figure 2-2: Flow chart of solution preparation, deposition thin films and post heat treatment steps.

Chapter 3: RESULTS AND DISCUSSION

3.1 Structural Characterization

3.1.1 X-ray Diffraction Results (XRD)

X-ray diffraction pattern of the Barium Titanate (BTO) films is measured using Bruker D8 ADVANCE X-ray (Cu-Kα) diffractometer and the results are presented in Figure 3-1. The XRD pattern exhibited a set of diffraction lines attributed to a tetragonal structure with the space group P4mm confirmed from the JCPDS card number 081-2203. Besides the diffraction peaks of Nb(0.07%):STO(100) substrate, no other stray phases can be seen. The (001) and (002) peaks in XRD pattern shows a BTO film with uniformly *c*-axis orientation. The XRD spectrum of all compositions on Nb(0.07%):STO(100) is given in Figure 3-2.



Figure 3-1: XRD pattern of BT thin-film on Nb(0.07%):STO(100) compared to Barium Titanate tetragonal structure. (JCPDS:081-2203)



Figure 3-2: XRD spectrum of a) BTiO₃ b) Ba_{0.5}Sr_{0.5}TiO₃ c) Ba_{0.7}Sr_{0.3}TiO₃ films grown on Nb(0.07%):STO (100) substrate.

3.1.2 Scanning Electron Microscopy (SEM) Results

Micrographs of the thin films surface are characterized using Leo G34 Supra 35VP. Using the scanning electron microscopy, we were able to evaluate the surface morphology of the samples. The scanning electron microscopy (SEM) is a precious tool to inspect the progress of the film deposition process. After each step of process, the thin film quality is inspected with SEM. As a result, various parameters including: the solution concentration, sintering temperature and atmosphere, electrode deposition quality and film thickness are optimized based on the obtained surface quality. In this regard, in solution concentration step, the deposited thin films were obtained from 1M solution. This solution was not stable enough and obtained films either were undesirably thick. Also, after spin coating, remnant solution crystallized on the surface of the film was not preferable (Figure 3-3). It was due to the insufficient speed rate of the spinning which couldn't spin off the solution properly. After that, the spin coating parameters were optimized in order to get homogenous films with repeatable thickness. Figure 3-4 (a) shows the surface of the film and Figure 3-4 (b) shows the vertical cross section of the film. The obtained results were repeated for all films. However, the porosity of the films was obtained in O₂ reach environment with heating rates and sintering atmosphere were tested. The optimum result was obtained in O₂ reach environment

Figure 3-5 (a) represents the thickness of the film in 50kX magnification. The thickness was measured as~200nm (

Figure 3-5 (b)).



Figure 3-3: SEM image of thin-film deposited from 1M solution in different magnification a) 10kX b) 30kX.



Figure 3-4: SEM results show a) surface morphology of thin film b) vertical cross section of the film.



Figure 3-5: Thickness of a dense thin film in a) 50kX magnification, b) 150kX magnification after optimizing the spin coat parameters, heating rate, sintering temperature and atmosphere.



Figure 3-6:a) deposited Au electrodes on BT b) Surface morphology of Au Electrode on BTO c) Schematic illustration of the Au Electrode and film structure in presence of the a very thin Cr adhesive layer

Although the surface quality of the deposited Au electrodes was promising and no significant physical defect observed in the SEM results, the obtained I-V results for the Au electrode were not satisfying which could correlate to the presence of the Cr adhesive layer. Since the addition of the dead layer adds an extra complication to the current leakage mechanism and the first attempts to get reasonable data were not successful, in entire measurements only Pt and Cu were used as top electrode.

Figure 3-7, 3-8, 3-9 represent the surface quality of the films in two magnifications after heat treatment in O_2 rich environment and before depositing the electrodes. Surface of the film is nearly porous free and no extra crystallization is observed.







Figure 3-8: Surface of Ba_{0.7}Sr_{0.3}TiO₃ film in two magnifications. a)10kX b)50kX.



Figure 3-9: Surface of Ba_{0.5}Sr_{0.5}TiO₃ film in two magnifications. a)10Kx b)50Kx.

The Platinum electrode surface quality is represented as an example in Figure 3-10. Diameter of each electrode is 100µm and Secondary Electron mode (SE2) and Inlens mode of SEM are used to inspect the deposited electrode quality. The electrodes are crack free and homogenous.



Figure 3-10: Platinum Electrode surface quality a) Diameter of each electrode is $100\mu m$ b) SE2 and Inlens images of the electrode surface.

3.2 Electrical characterizations

An in-house electrical measurement setup was assembled partly at Sabanci University followed by systematic measurement efforts at the National Institute of Materials Physics (NIMP) in Magurele, Romania. The equipment in Sabanci University includes a LCR meter model HIOKI IM3523, a sourcemeter model KEITHLEY 2401, a digital multimeter model Agilent 34401A and a triple output DC power supply model Agilent E3631A, a stereotype microscope model CARL ZEISS STEMI 2000, two precise micromanipulator (Figure 3-11).



Figure 3-11: Electrical measurement setup in Sabanci University.

The equipment at NIMP includes a Lakeshore cryoprober model CPX-VF, a Keithley electrometer model 6517 (for *I–V* characteristics), a ferroelectric tester model TF2000 from aixACCT (for hysteresis loops), and an impedance analyzer model HP 4194A for C–V measurements (Figure 3-12).



Figure 3-12: Electrical measurement setup in NIMP. Magurele-Romania.

Hysteresis measurements were performed using a triangular voltage wave with 1 kHz frequency. The C V measurements were performed using an AC small signal of 0.1 V amplitude and 100 kHz frequency. *I–V* measurements were performed mainly using hysteresis type measurements $(0 \rightarrow (+V_{max}) \rightarrow 0 \rightarrow (V_{max}) \rightarrow 0)$. Sweeping down part from V_{max} to 0 gives a reasonable estimation of the potential barriers because this part did not contain a parasitic contribution from polarization switching[91]. The electric measurements included current–voltage (*I–V*) characteristics, capacitance–voltage (*C–V*) and hysteresis loop (*P–E*). The electrical characterization was performed in two configurations of the probes: configuration 1 and 2. In configuration 1, the DC voltage applying probe was connected to the top electrode and the substrate acted as the bottom electrode (grounded). In configuration 2, two top electrodes were connected side by side. Figure 3-13 illustrates two configurations of probes.



Figure 3-13: Two different configurations for probes to measure the I-V and C-V characteristics.



Figure 3-14: Samples assembled on the standard stage in form of configuration 1.

3.2.1 Current-Voltage (I-V) Characteristics

The I-V Characteristic is short for current-voltage characteristic curves of an electrical device or a component. As its name suggests, I-V characteristic curves show the relationship between the current flowing through an electronic device and the applied voltage across its terminals. According to Ohm's Law, when the voltage across the resistor increases, the current flows through it too. Constructing a graph to show the relationship between the voltage and current of the resistive elements displays a linear curve is which slope is the resistive magnitude. However, many electronic components have non-linear characteristics (I/V ratio is not constant). Semiconductor diodes can be characterized by non-linear current-voltage characteristics as the current flowing through a forward-biased. The absolute value of current in I-V measurements for three different compositions (BT,BST70,BST50) and two different probes configuration (1 and 2) in room temperature is presented in Figure 3-15, Figure 3-16, Figure 3-17, Figure 3-18 and Figure 3-19. All results show a rectifying behavior of the contacts, which indicate the diode like behavior of the Pt(Cu)-B_xSr_{1-x}T-Nb:STO heterostructure. It means that the junction is not ohmic contact and has characteristics which can be treated as a Schottky junction. In configuration 1, bipolar resistive switching behavior in Pt-B_xS_{1-x}T-Nb:STO heterostructure is more obvious by hysteretic current–voltage (I–V) curve. In these graphs, for all compositions, the switching hysteresis in the positive voltage region is more notable than that in the negative region. Also, a distinguished jump is observed in positive region which can be correlated to the polarization reversal and consequent change of barrier height (BH) which will be discussed in detail in section 3.2.5. In addition, leakage current in configuration 2 for all compositions and electrode combinations are lower than that of configuration 1, because the contribution of the semiconductor bottom electrode is significantly eliminated as both electrodes are symmetrical in configuration 2. Considering the Pt electrode deposited samples (configuration 1 and 2) in Figure 3-18 (a) and Figure 3-19 (a) increasing the Sr content systematically decrease the leakage current, since Sr content decrease the polarization which leads to lower leakage current [92]. However, such noticeable trend is not observed in Cu electrode samples (configuration 1 and 2) in Figure 3-18 (b) and Figure 3-19 (b). Although the Markondeya et al. [93] reported that increasing the Sr content in Cu electrode samples also increases the leakage current.



Figure 3-15: I-V measurements data of BT with a) Pt as the top electrode and b)Cu as the top electrode compared in configurations 1 and



Figure 3-16: I-V measurements data of BST70 with a) Pt as the top electrode and b) Cu as the top electrode compared in configurations 1 and 2.



Figure 3-17: I-V measurements data of BST50 with a) Ptas the top electrode and b) Cu as the top electrode compared in configurations 1 and 2.



Figure 3-18: Measured I-V characteristics of all compositions in configuration 1 for both a)Pt as the top electrode b)Cu as the top electrode.



Figure 3-19: Measured I-V characteristics of all compositions in configuration 2 for both a)Pt as the top electrode b)Cu as the top electrode.

3.2.2 Capacitance – Voltage characteristics

To get further insight into the electrical characteristics of Pt(Cu)-Ba_xSr_{1-x}T-Nb:STO heterostructure, the capacitance–voltage (C–V) measurement carried out at frequency of 1 kHz for all samples in room temperature. In Figure 3-20, electrode configuration 2 is selected for C-V measurements of the samples in room temperature. The reason is that the I-V result for the configuration 2 revealed a lower leakage current and it means better charge storage characteristics. Also, since the aim is to get capacitance of the junction and emphasize on the polarization switching, selecting configuration 2 is rather more reasonable. Figure 3-20 and Figure 3-21 show the C–V curve with an obvious ferroelectricity behavior by negative/positive voltage. The samples with Pt as top electrode show a higher capacitance compared to Cu deposited samples. For both Pt and Cu deposited samples, BST50 shows higher capacitance compared to BT (Figure 3-20). The reason is that addition of the Sr decrease the Curie point which leads to increase in dielectric constant and capacitance. However, the effect of Sr addition and increasing capacitance cannot be correlated for BST70.

Figure 3-20: C-V measurement of the samples in room temperature (Configuration 2). Comparing the Cu and Pt electrode type on a) BaTiO₃ (BT) b) Ba_{0.7}Sr_{0.3}TiO₃(BST70) c)Ba_{0.5}Sr_{0.5}TiO₃(BST50). Figure 3-21: C-V measurementof the samples in room temperature (Configuration 2). Comparing the effect of: a) Cu as the top electrode b) Pt as top the electrode on all compositions. c) Comparing all C-V measurements result.

3.2.3 Polarization – Electric field (P-E) Hysteresis Loop

The Polarization–Electric field hysteresis loops (P-E) and current associated with it for the BT, BST70 and BST50 samples (Pt top electrode) at room temperature (RT) are presented in Figure 3-22.



Figure 3-22: P-E hysteresis loop for Pt top electrode of a)BT b)BST70 c)BST50.

The P-E loops obtained by using a triangular voltage with a frequency of 1 kHz are presented in Figure 3-23.

Figure 3-23: Time dependent P-E hysteresis loop for Pt top electrode of a)BT b)BST70 c)BST50.

The P-E loop recorded during hysteresis measurement does not show the expected theoretical characteristics associated with polarization reversal. Before this, the trace of polarization switching is confirmed by C-V measurements. Also comparing the I-V results with computational modeling confirms the existing of the polarization switching in the samples. Hence, no further interpretation can be made from the P-E results. However, Pintilie *et al.*[94] reported an indication for the polarization switching in the hysteresis observed on the negative side of the I-V characteristic during polarization hysteresis measurement without trace of switching in positive bias.



Figure 3-24: Hysteresis loops for SRO-PZT-Ta structures with 100 nm thickness of the PZT layer [94].

They claim that the switching peaks might be hidden by the large leakage current obtained for the negative bias polarity. However, I-V characteristic presents a strong rectifying behavior very similar to a Schottky diode in negative bias (Figure 3-24). In this method, by applying bias in $0 \rightarrow -V_{max}$ current measurement contain not only the switching of polarization contribution, but also other contributions such as leakage current and dielectric displacement. By applying a pulse of the same amplitude and same polarity after the previous one $(0 \rightarrow -V_{max})$, the second measurement contain back switched dipole contribution in addition to the leakage contribution and dielectric displacement. In consequence, the difference between the polarization measured after first pulse and second pulse will be the remnant polarization. This is a plausible method to obtain the value of real polarization at 0 electric field or saturated polarization. Hence, the same method is applied on the Pt electrode deposited samples and the results are presented in Figure 3-25. The polarization is decreased in BT and BST50 by addition of Sr which was already expected. The remnant polarization in BT and BST50 are 2 and 44 μ C/cm²respectively. The magnitude of the polarization in BST70 is extremely high for such composition and compared to BT and BST50. This can only be the sign of high current leakage in this sample. In this thesis, the presence of polarization switching is confirmed with the C-V result and remnant polarization results obtained by mentioned method.



Figure 3-25: Remnant polarization indication in Pt deposited electrode on a) BT b) BST70 c)BST50.
3.2.4 Computational Modeling Results

The results of the numerical computation for the system of interest in this thesis are used to shed light especially on the I-V behavior of our samples where we did see indications of the RS phenomena. The schematic of the system analyzed has already been provided in Figure 1-15. The thermodynamic model coupled with the universal equations of electrostatics and equations of semiconductors allow us to obtain results without any apriori assumptions that is the case with several other works. The most important result we obtain is that the semiconductor Nb:SrTiO₃ electrode can take on one the two states at the FE interface depending on the direction of polarization: Either a carrier accumulated or a carrier depleted state. The two states provide different mechanisms of screening to polarization charges that is the basis of resistive switching. Here the result of the computational study presented in four graphs. In these graphs, polarization along the Z axis, carrier (electrons) distribution map, ionized donor distribution map and energy band diagram is presented. The detail information about the computational method is provided in the section 1.4.4. As it is observable from Table 4, even at zero bias interface of the BT and Nb:STO produce a junction which can be assumed as a Schottky junction. Applying +1V and -1V bias leads to the conduction, since the conduction band penetrates to the Fermi level. The result of the data is directly correlated to the RS behavior and discussed in detail in section 3.2.5.



Table 4: Computational simulation results for BT in three different bias applied in top electrode.

BST50				
Voltage (v)	Polarization along Z axis	carries (Electron) map	lonized donors distribution map	Energy band diagram
0	Current and a set of the set of t	Sectore and Lever and Control of Sectore and Sectore a	a a a a a a a a a a a a a a a a a a a	
-1	Management and a set of the set o	The second parameterization of the second parameterization of	The second parameters of the second parameters	Line and the second sec
+1		a contraction of the second se	a a a a a a a a a a a a a a a a a a a	

Table 5: Computational simulation results for BST50 in three different bias applied in top electrode.

Here, only the results of BT and BST50 are presented. The BST70 experimental results were not satisfactory compared to BT and BST50. Hence, we weren't able to correlate a scientific explanation between computational and experimental studies.

The penetration of conduction band to Fermi level in BT (+1V bias) is much abrupt compared to the one in BST50. It is because of the fact that in BT the polarization switching is not fully satisfied (consider PZF) and band bending is much more significant. This significant band bending in BT means that it has more electron accumulation in the interface therefore we expect higher amount of leakage current. In this model, due to the polarization, the charge accumulation in BT is much more higher compared to the one in BST50 in which we have smoother band bending. This is consistent with the experimental results. It is worth to note that, the change in leakage current in the experimental study is not higher than one order of magnitude between BT and BST50. However, as mentioned earlier, this model is an ideal thermodynamic model which was only proposed to clarify some facts about ferroelectric polarization state and corresponding mechanism proposed in this thesis. In addition, this computational study confirms the experimental study results and explains the band bending, carrier accumulation and depletion which only semi qualitatively explains the proposed mechanism. The misfit in our computational study is 1% which is a well-accepted value for such systems which in real life might be higher value.

3.2.5 Resistive Switching

The Figure 3-26 is presented here as an example of resistive switching and in purpose of discussing the mechanism. The following I-V curve is obtained from hysteresis loop and is a dynamic measurement which whole measurement is done in 1s. However, in the regular I-V curve (Figure 3-27) with changing the DC bias the measurement is quasi static with 0.5s step size. In Figure 3-27a clear jump in current is observable near the polarization switching point.



Figure 3-26: I-V curve corresponding to Pt-B_xS_{1-x}T-Nb:STO heterostructure indicating the direction of the polarization and resistive states.



Figure 3-27: Regular I-V curve corresponding to Pt-B_xS_{1-x}T-Nb:STO heterostructure measured in configuration 1.

From Figure 3-26 it is noticeable that the resistive state of the Pt-B_xS_{1-x}T-Nb:STO heterostructure changes either with increasing positive bias or negative bias which is opposite to reported studies for ferroelectric tunnel junctions [95], [96]. It seems the ferroelectric interface junction is more dominant than ferroelectric tunneling effect which is reported for the Pt-B_xS_{1-x}T-Nb:STO heterostructure [97]. It seems the current in $0 \rightarrow +V_{max}$ (or $-V_{max} \rightarrow 0$) is lower than $-V_{max} \rightarrow 0$ (or $0 \rightarrow +V_{max}$) which may correspond to the high resistive and low resistive respectively. It can be correlated to the resistive switching in Pt-B_xS_{1-x}T-Nb:STO heterostructure [81], [98], [99]. In order to investigate the RS [100]–[102] it has to be taken into account that a Schottky barrier can form at the interface between the electrode and semiconducting ferroelectric oxide (in this case: Ba_xSr_{1-x}TiO₃). The profile of the Schottky barrier can be altered by reversing the polarization direction. Based on computational study results, the ferroelectric polarization could be the responsible for the different resistive states.



Figure 3-28: Schematic charge distribution and (c), (d) corresponding energy-band diagrams at LRS and HRS, respectively. In the BTO layer, the red arrows denote the polarization directions and the large 'plus' and 'minus' symbols represent positive and negative ferroelectric bound charges, respectively. The 'plus' and 'dot' symbols in the Pt and NSTO represent holes and electrons, respectively. The 'circled plus' symbols represent ionized donors [103].

As illustrated schematically in Figure 3-28, when the polarization is pointed to the semiconductor, positive bound charges accumulate at the Ba_xSr_{1-x}TiO₃-Nb:STO interface and negative carriers (electrons) in Nb:STO (considered n-type SC) are attracted to the positive bound charges which makes the Nb:STO interface accumulated. In addition, the screening of negative bound charges is incomplete and a depolarizing field opposite to the polarization at the interface of the FE lowers the barrier height (BH) and hence leads to the band bending at the interface (Figure 3-28)[104]. This result is in agreement with our computational results presented in Figure 3-29 obtained by the thermodynamic simulations (For the methodology, see Section1.4.4). Due to that band bending and lower BH at Ba_xSr_{1-x}TiO₃-Nb:STO, the carriers can easily pass through the region which is under influence of the electric field. As a result, the current pass in low resistive state (LRS).



Figure 3-29: Result of the computational study in applying +1V bias to the top electrode BT a) Polarization along Z axis b)carriers (Electron) map c) lonized donor distribution map d) energy band diagram in the midsection of the film along the thickness.

On the other hand, when the polarization is pointing to the metal electrode (Pt), the negative carriers (electrons) in Nb:STO (n-type SC) are repelled by the negative bound charges at $Ba_xSr_{1-x}TiO_3$ -Nb:STO and immobile screening charges (Ionized donors) spread over a space charge region (depletion state) depending on the doping profile. Incomplete screening produces a depolarizing field and increases the BH (Figure 3-30). Consequently, the space charge region and BH will increase when increasing the polarization which leads to the high resistive state (HRS).



Figure 3-30: Result of the computational study in applying -1V bias to the top electrode a) Polarization along Z axis b)carriers (Electron) map c) lonized donor distribution map d) energy band diagram in the midsection of the film along the thickness.

In theory, starting from 0 bias and moving either to $+V_{max}$ or $-V_{max}$ does not change the RS effect and in both cases one shall see the RS. In this thesis, all measurements are kept constant profile form $0 \rightarrow +V_{max} \rightarrow 0 \rightarrow -V_{max} \rightarrow 0$. The same mechanism is valid for the case of BST50 and correlation between computational studies and experimental is the same. However, the difference is the Sr content which

affects the polarization and thus the resistive switching, in which increasing the Sr content, decrease the polarization magnitude [105]. Based on the regular I-V results, the resistive switching magnitudes in BT and BST50 (Pt electrode, configuration 1) are very close to each other in which the magnitude in BST50 is one order of magnitude higher. The tricky point to interpret the computational data is that the applied voltage abruptly changes from +1V to -1V, and the assumption is that the system is in equilibrium in either applied biases. In other words, from+1V to -1V, system fully changes its polarization state. In such thermodynamical model, ±1V are the only boundary conditions to change the polarization and consequently change in the electron distribution and ionized donor states near the interface. By means of this, we correlate the computational results to the mechanism we proposed for the resistive switching which is the effect of the polarization change on the barrier height (BH) and consequently charge flow in the system.

Chapter 4: CONCLUSION

Ferroelectricity is a property of materials that possess spontaneous electric polarization. It is due to the presence of electric dipoles and applying external electric field changes the polarization. Barium Strontium Titanate (BST) in different compositions ($Ba_xSr_{1-x}TiO_3$) compared to $BaTiO_3$ which is a solid solution of Barium Titanate ($BaTiO_3$) and Strontium Titanate ($SrTiO_3$) and can be formed in an entire range of x (x=0,0.3,0.5). The ferroelectric transition temperature is nearly a linear function of Ba/Sr ratio. Adding Sr into BTO structure causes the paraelectric to ferroelectric transition temperature T_c decrease linearly (30% of STO) and the transition happens near the room temperatures.

The topic of this thesis forms a part of TUBITAK 1001 COST project no 113M792. The main motivation of this project is to address the combined effect of strontium content and electrode type on electrical properties of Ba_xSr_{1-x} TiO₃ thin-films.

For preparing the barium strontium titanate ($Ba_xSr_{1-x}TiO_3$) solution, the sol-gel method and for depositing homogenous films, the spin coating technique were used. Parameters of the spin coating were optimized in order to get homogenous thin films. Heat treatment of the films required slow rate of heating in an oxygen rich environment. In order to inspect the thin films quality, SEM characterization is crucial. Here, some key parameters were optimized based on the SEM results.

The I-V measurement is an important technique to obtain valuable information about the electrical characteristics of the Metalferroelectric metal interfaces. This includes the leakage current. In this thesis, for measuring the I-V characteristics two probe configurations were used. Leakage current in configuration 2 for all compositions and electrode combinations were lower than that of configuration 1, because the contribution of the semiconductor bottom electrode was significantly eliminated as both of the electrodes were symmetrical in configuration 2. Furthermore, effect of the two metallic top electrodes was investigated. Considering the Pt electrode deposited samples (configuration 1 and 2) increasing the Sr content systematically decreased the leakage current, since Sr content decreased the polarization which led to lower leakage current. However, such noticeable trend was not observed in Cu electrode samples (configuration 1 and 2).

As we expected, C-V measurements were very useful tool to indicate the polarization. The butterfly shape in C-V measurements was found to be a sign of polarization. The samples with Pt as top electrode showed a higher capacitance compared to Cu deposited samples. For both Pt and Cu deposited samples, BST50 showed higher capacitance compared to BT. The reason for that was the addition of Sr decreasing the Curie point which led to an increase in dielectric constant and capacitance. However, the effect of Sr addition and increase in capacitance could not be explained for BST70.

Furthermore, we developed the thermodynamic theory for a given $Ba_{1-x}Sr_xTiO_3$ composition using the phenomenological coefficients of $BaTiO_3$ and $SrTiO_3$. By changing the boundary conditions, we showed a change in polarization states, carrier distribution, ionized donor states and band bending near the interface. In Chapter 3, only the results of BT and BST were presented. The BST70 experimental results

were not satisfactory compared to BT and BST50. Hence, we weren't able to correlate a scientific explanation between computational and experimental studies.

The penetration of conduction band in Fermi level indicates the electron accumulation state in the interface and hence the polarization state can be determined. Here we found a good agreement between the experimental study and the proposed computational model. We should note that, this model is an ideal thermodynamic model which was only proposed to clarify some facts about ferroelectric polarization state and corresponding mechanism proposed in this thesis. In addition, this computational study confirmed the experimental study results and explained the band bending, carrier accumulation and depletion which only semi qualitatively explained the proposed mechanism. The misfit in our computational study was 1% which is a well-accepted value for such systems which in real life might be higher values.

Based on the regular I-V results, the resistive switching magnitudes in BT and BST50 (Pt electrode, configuration 1) were very close to each other in which the magnitude in BST50 was one order of magnitude higher. The tricky point to interpret the computational data was that the applied voltage abruptly changed from +1V to -1V, and the assumption was that the system was in equilibrium in either applied biases. In this model, ±1V were the only boundary conditions to change the polarization and consequently change the electron distribution and ionized donor states near the interface.

By means of this, we correlated the computational results to the mechanism we proposed for the resistive switching which was the effect of the polarization change on the barrier height (BH) and consequently charge flow in the system. In this work, we focused on the experimental evidence for resistive switching in the light of thermodynamic theory of these systems and discussed the effect of Sr content on current-voltage characteristics of our samples.

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