

# A 77 GHz On-chip Strip Dipole Antenna Integrated with Balun Circuits for Automotive Radar

Ibrahim Tekin

Electronics Engineering  
Sabanci University  
34956, Tuzla , Istanbul, Turkey  
tekin@sabanciuniv.edu

Mehmet Kaynak

Technology/Process Integration  
IHP GmbH  
15236 Frankfurt(Oder), Germany  
kaynak@ihp-microelectronics.com

**Abstract**—In this paper, design and implementation of a 77 GHz on-chip strip dipole antenna integrated with both lumped and transmission line based balun circuits are presented. The on-chip antenna is realized by using IHP's 0.25  $\mu\text{m}$  SiGe BiCMOS technology with localized back-side etch (LBE) module to decrease substrate loss. The strip dipole antenna is fed by both a lumped LC circuit and strip line tapered baluns integrated on the same substrate and occupies an area of  $1 \times 1.2 \text{ mm}^2$  including the RF pads. For increased directivity, the antenna sits on a grounded silicon substrate. Experimental results show that antenna is well matched around the design frequency and achieves 7 GHz impedance bandwidth (minimum return loss of 17 dB) for the LC balun circuit. The antenna and its feeding structure are well suited for 77 GHz single chip automotive radar applications.

## I. INTRODUCTION

With the advance of silicon based technologies (CMOS circuits up to 100 GHz, SiGe Circuits reaching to almost 1 THz), we see more civilian use of millimeter wave radar especially in navigation, traffic control and safe highway driving. Inline with these developments, ETSI has developed standards for "short range" radar for automotive applications in 24 – 77 GHz bands. In 77 GHz band, there is a 2 GHz bandwidth allocated for an application of a short range automotive radar for the purposes of stop and go, blind side detection, crash avoidance, braking if crash cannot be avoided and to keep safe driving distance with the traffic ahead, [1]. As the frequency is increased, the size of the antenna becomes comparable to the chip size (less than 1 mm) and this brings the opportunity for a highly integrated single chip transceiver integrated with the antenna or antenna arrays.

There is extensive research on on-chip antennas for 77 GHz band for unbalanced type of antennas (such as slot or microstrip patch type of antennas) as well as feeding balun structures for circuit application. In [2], single-ended fed antennas for 77 GHz operation are reported. In [3-4], balun structures are shown for 77 GHz band for small chip area. In [5], a small chip size balun circuit is mentioned to be used with the antenna; however, antenna design is not specified. Especially, when wire/strip type antennas and feeding balun structures on the same chip are in the vicinity of each other, the radiation properties of the antenna is highly affected by the

metallic structures of the balun circuit. Hence, this requires the design of both the wire/strip antenna and the balun structure, together. For full integration of mm-wave circuits with the properly designed antenna, the overall chip size is still a problem and has to be small. In this paper, a strip dipole on-chip antenna is designed with the integrated balun structures (both lumped and stripline distributed) to obtain a small sized chip. A standard BiCMOS process with an additional LBE module is used to realize the antenna. Silicon substrate below the antenna is etched away to eliminate the substrate loss, while a ground plane is placed below the silicon substrate for increased directivity/gain.

## II. ON-CHIP STRIP ANTENNA AND BALUNS

A microphotograph of the on-chip dipole antenna integrated with the strip line balun is shown in Fig. 1, which is optimized for 77 GHz operation using HFSS version 12.0. The length of the strip dipole antenna is  $1100 \mu\text{m}$  and it has a width of  $100 \mu\text{m}$  with a metal thickness of  $3 \mu\text{m}$ . The feed gap between the arms of the dipole is  $40 \mu\text{m}$ . The dipole arms are deposited on a  $\text{SiO}_2$  layer of  $11.4 \mu\text{m}$ , which is placed above a low resistivity substrate Si (20 Ohm-cm) with a thickness  $670 \mu\text{m}$ . The silicon substrate is grounded and the silicon volume under the dipole antenna is removed.

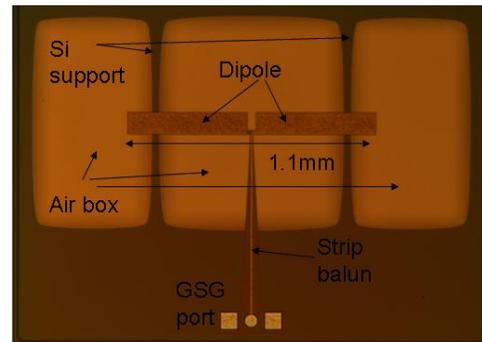


Fig.1 77 GHz dipole antenna and the strip balun

The silicon area which are closer than  $400 \mu\text{m}$  to the arms of the dipole antenna are removed by etching process and three air boxes are formed under the dipole antenna except two blocks of silicon substrate as mechanical supports. Fig. 1 also

shows the strip line balun formed by linearly tapering the ground plane of a regular microstrip line. Microstrip line length is  $800\ \mu\text{m}$  and has a width of  $15\ \mu\text{m}$ . The width of the microstrip ground line is  $100\ \mu\text{m}$  at the RF port and achieves  $15\ \mu\text{m}$  at the antenna feed point. Vertical profile of the antenna and the substrate etching is shown in Fig. 2.

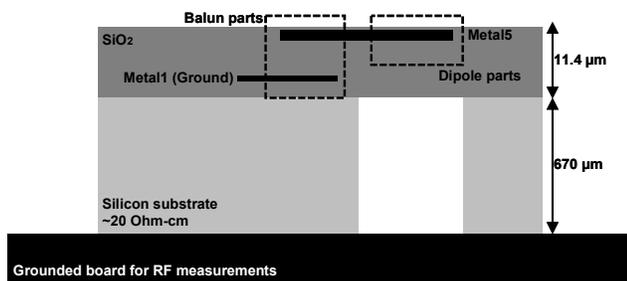


Fig.2. Etched substrate and the metal lines for the dipole

To decrease the total chip area, the on-chip strip dipole antenna is also integrated with the lumped LC balun as shown in Fig. 3. The LC balun converts the single ended signal to differential signals with passive components. The inductors are formed by thin microstrip lines, for capacitances, metal-insulator-metal (MIM) capacitors are used. For 77 GHz, S-parameters and phase simulations are performed using HFSS. An inductor value of  $120\ \text{pH}$  and a capacitor value of  $37\ \text{fF}$  are obtained for the balun structure. The balun structure occupies an area of  $250\ \mu\text{m} \times 250\ \mu\text{m}$  ( $800\ \mu\text{m} \times 100\ \mu\text{m}$  area is required for the strip balun). For measurement purposes, additional  $500\ \mu\text{m}$  microstrip line is connected to decrease the effect of RF measurement probe on the antenna radiation. Input impedance measurements are performed with a 110 GHz Agilent Network analyzer with GSG probe.

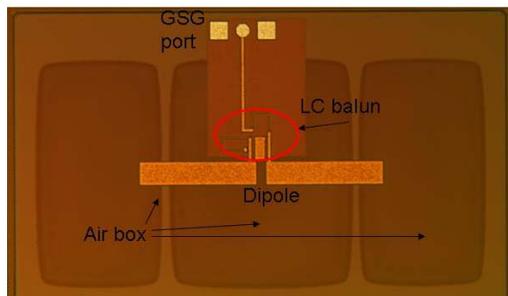


Fig.3 77 GHz dipole antenna and the LC balun

Simulated and measured  $S_{11}$  of the dipole antenna with strip balun are given in Fig. 4. Minimum return loss of  $15.6\ \text{dB}$  is simulated at  $75\ \text{GHz}$  with a simulated gain of  $3.6\ \text{dBi}$ . Return loss of  $9.7\ \text{dB}$  is measured at  $79\ \text{GHz}$  after deembedding the GSG RF pads. Simulated and measured  $S_{11}$  of the dipole antenna with LC balun are given in Fig. 5. Return loss of  $21.5\ \text{dB}$  is simulated at  $75\ \text{GHz}$  with a simulated gain of  $3.4\ \text{dBi}$ . Return loss of  $17.9\ \text{dB}$  is measured at  $74\ \text{GHz}$ . Simulated and measurement results agree quite well for the LC balun fed strip dipole antenna. In comparison to strip balun case, the LC balun fed dipole antenna is better matched and achieves  $4\ \text{GHz}$  impedance bandwidth at  $75\ \text{GHz}$ . Note that by implementing

varactors instead of capacitances, one can tune the center frequency of the LC balun. The antennas are already fabricated and being measured for  $S_{11}$  and radiation patterns at  $75/77\ \text{GHz}$ . Further detailed results will be presented at the

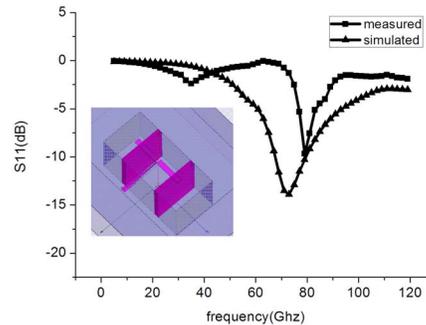


Fig.4  $S_{11}$  of 77 GHz dipole antenna and the strip balun

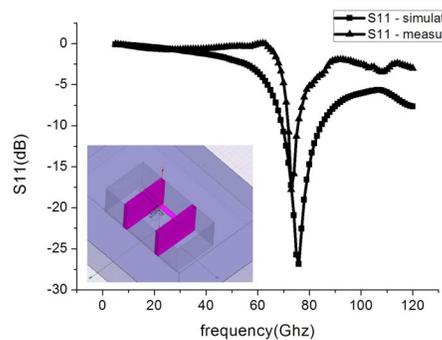


Fig.5  $S_{11}$  of 77 GHz dipole antenna and the LC balun

#### ACKNOWLEDGEMENT

This work was supported by Turkish Scientific/Technology Institution TUBITAK Grant 111E061.

#### REFERENCES

- [1] ECC decision of 19 March 2004 on the frequency band 77-81 GHz to be designated for the use of automotive short range radars (ECC/DEC 04) Eur. Radiocommun. Office. Copenhagen, Denmark, 2004, Online, <http://www.ero.dk>
- [2] M.R. Ahamdi and S. S. Naeini, "On-chip Antennas for 24, 60, and 77GHz Single Package Transceivers on Low Resistivity Silicon Substrate", 2007 IEEE Antennas and Propagation Symposium, pp. 5059-5062, June 2007, Hawaii, USA
- [3] Jong-Sik Lim, et.al, "A 77 GHz CPW Bbalun using Wilkinson structure", 34<sup>th</sup> European Microwave Conference, pp. 377-380, Oct. 2004, Amsterdam.
- [4] C.-H. Liu, C.-Y. Hsu, H.-R. Chuang and C.-Y. Chen, "A 60-GHz millimeter-wave CMOS marchand balun using 0.18 $\mu\text{m}$  CMOS technology", Microwave and Optical Technology Letters, Vol. 51. No.3, pp.766-770, March 2009.
- [5] Cheng-Ying Hsu, Chu-Yu Chen, and Huey-Ru Chuang, "A 77-GHz CMOS On-Chip Bandpass Filter With Balanced and Unbalanced Outputs" IEEE Electron Device Letters, Vol. 31, No.11, pp.1205-1207, Nov. 2010